Unit in mm

TOSHIBA Diode Silicon Epitaxial Planar Type

HN1D02FU

Ultra High Speed Switching Application

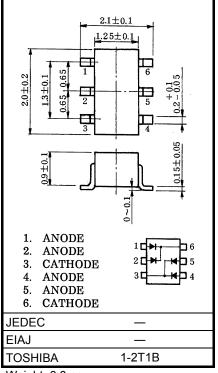
• HN1D02FU is composed of 2 unit of cathode common.

• Low forward voltage : $V_{F(3)} = 0.90V$ (typ.) • Fast reverse recovery time: $t_{rr} = 1.6ns$ (typ.)

• Small total capacitance : $C_T = 0.9pF$ (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V_{RM}	85	V
Reverse voltage	V _R	80	V
Maximum (peak) forward current	I _{FM}	300*	mA
Average forward current	Io	100*	mA
Surge current (10ms)	I _{FSM}	2*	Α
Power dissipation	Р	200	mW
Junction temperature	Tj	125	°C
Storage temperature	T _{stg}	-55~125	°C



Weight: 6.8mg

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

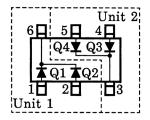
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: This is the Absolute Maximum Ratings of single diode (Q1 or Q2 or Q3 or Q4). In the case of using Unit 1 and Unit 2 independently or simultaneously, the Absolute Maximum Ratings per diode is 75% of the single diode one.

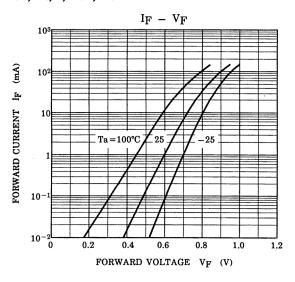
Electrical Characteristics (Q1, Q2, Q3, Q4 Common, Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V _{F (1)}	_	I _F = 1mA	-	0.60	-		
	V _{F (2)}	_	I _F = 10mA	-	0.72	1	V	
	V _{F (3)}	_	I _F = 100mA	_	0.90	1.20		
Reverse current	I _{R (1)}	_	V _R = 30V	-	1	0.1	μΑ	
	I _{R (2)}	_	V _R = 80V	-	-	0.5		
Total capacitance	C _T	_	V _R = 0, f = 1MHz	_	0.9	3.0	pF	
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	_	1.6	4.0	ns	

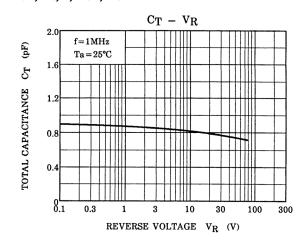
Pin Assignment (Top View)



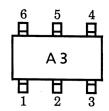
Q1, Q,2, Q3, Q4 Common



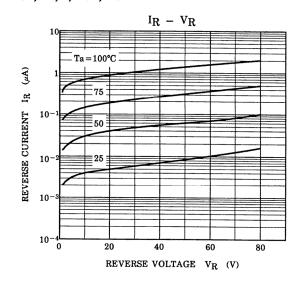
Q1, Q,2, Q3, Q4 Common



Marking



Q1, Q,2, Q3, Q4 Common



Q1, Q,2, Q3, Q4 Common

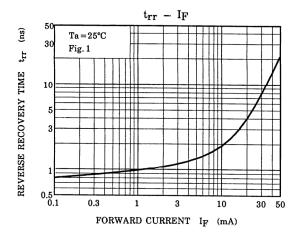
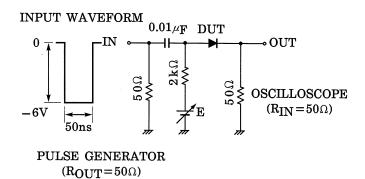
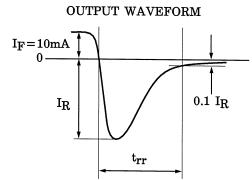


Fig.1 Reverse Recovery Time (t_{rr}) Test Circuit





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